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L BENINI - Integrated Computer-Aided Engineering, 1998 - IOS Press
... Just one **RTL** simulation was required to obtain the ... context but it is not applicable to static **power estimation**. ... Assume that the **circuit** is **stable** at time t 1 ...
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Node sampling: a robust **RTL power** modeling approach - group of 8 »
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... assume T = 1 and It'drl =: 1, thus energy and **power** have ... not an **RTL** model for e. We observe, howcver ... its mean valuc e, thus solving the energy **estimation** problem ...
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... the effect of switching activities in our **power estimation**. ... difference between **Circuit** 1 and **Circuit** 2 i ... CLK3, 3.2 Timing Relationships and **Power** Consider two ...
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A multiple clocking scheme for low-**power RTL** design - group of 8 »
CA Papachristou, M Nourani, M Spining - Very Large Scale Integration (VLSI) Systems, IEEE ..., 1999 - ieeexplore.ieee.org
... and frequency , we synthesize a group of **RTL circuit** structures implementing ... nonoverlapping clocks, respec- tively, and then **estimate** the **power** of each **RTL** ...
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Power management techniques for control-flow intensive designs - group of 10 »
A Raghunathan, S Dey, NK Jha, K Wakabayashi - Proc. Design Automation Conf, 1997 - doi.ieeecomputersociety.org
... **Power**): S0 S1 S2 S3 S4 S5 tempX+dX ... (b) (c) (a) Figure 1: (a) **RTL circuit** implementing the ... during which the controller makes the state **transition** S 2 ! S 3. ...
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Extracting **RTL** Models from Transistor Netlists - group of 8 »
KJ Singh, PA Subrahmanyam - Proceedings of the 1995 IEEE/ACM international conference on ..., 1995 - doi.ieeecomputersociety.org
... not concerned with modeling the transient behavior in the **RTL** model (we assumed that the **circuit** has time ... 3(c) where there is a direct **transition** from one ...
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RTL level preparation of high-quality/low-energy/low-**power** BIST - group of 5 »
MB Santos, IC Teixeira, JP Teixeira, S Manich, R ... - Test Conference, 2002. Proceedings. International, 2002 - ieeexplore.ieee.org
... a metric that is extensively used to **estimate** energy and ... E0 be the energy consumed by the **circuit** during the ... Mask Influence on Energy / **Power** Con- sumption The ...
Cited by 5 - Web Search

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... current drawn by a logic **gate**, so that the short-**circuit** current during ... triggers the latches, some of them will make a **transition** and will draw **power**. ...
Cited by 330 - Web Search

Power modeling for high-level **power estimation** - group of 20 »
S Gupta, FN Najm - Very Large Scale Integration (VLSI) Systems, IEEE ..., 2000 - ieeexplore.ieee.org
... for an -input combina- tional **circuit** is 2 ... **transitions** into clusters of input **transitions** that have the ... to **estimate** switching activity and **power** consumption at ...
Cited by 134 - Web Search - Library Search - BL Direct

Estimation of average switching activity in combinational and sequential circuits - group of 3 »
A Ghosh, S Devadas, K Keutzer, J White - Proceedings of the 29th ACM/IEEE conference on Design ..., 1992 - portal.acm.org
... the Boolean conditions that cause glitching (multiple **transitions** at a **gate**) in the **circuit**. ... a significant percentage of the dissipated **power** or switching ...
Cited by 267 - Web Search

Register-transfer level **estimation** techniques for switching activity and **power** consumption - group of 15 »
A Raghunathan, S Dey, NK Jha - Proceedings of the 1996 IEEE/ACM international conference on ..., 1997 - portal.acm.org
... 0 **transition** as half a **transition**, leading to ... running CSIM on the entire **gate**-level **circuit**. ... **RTL power estimation** methodology The flowchart shown in Figure 2 ...
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Short-**circuit power** dissipation **estimation** for CMOS logic gates - group of 2 »
SR Vemuru, N Scheinberg - Circuits and Systems I: Fundamental Theory and Applications, ..., 1994 - ieeexplore.ieee.org
... ratios of S and for **gate** lengths of ... Short-**circuit power** dissipation of CMOS inverter obtained from ... simulations for various input **transition** times, capacitive ...
Cited by 41 - Web Search

A Monte Carlo approach for **power estimation** - group of 6 »
R Burch, FN Najm, P Yang, TN Trick, TI Inc, TX ... - Very Large Scale Integration (VLSI) Systems, IEEE ..., 1993 - ieeexplore.ieee.org
... T) be the number of **transitions** of x ... s VddCi———, where C, is the total capacitance at i. The total average **power** dissipated in the **circuit** during the ...
Cited by 185 - Web Search

Towards a high-level **power estimation** capability [digital ICs] - group of 4 »
M Nemani, FN Najm - Computer-Aided Design of Integrated Circuits and Systems, ..., 1996 - ieeexplore.ieee.org
... V is the average node **transition** density, defined ... then a viable high- level **power estimation** methodology would ... simulation of the sequential **circuit** to measure ...
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Google Scholar BETA toggle circuit power transition gate | RTL estii 1970 - 2000 Search Adv Sch Sch

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FN Najm - Very Large Scale Integration (VLSI) Systems, IEEE ..., 1994 - ieeexplore.ieee.org
... inputs are propagated into the **circuit** assuming spatial independence and the **power** was computed ... zero-delay model was used, the **toggle power** was ignored ...
Cited by 330 - Web Search

Feedback, correlation, and delay concerns in the **power estimation** of VLSI circuits - group of 10 »

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... Computing the **toggle power** is one main challenge in **power** esti ... How exactly do the **circuit** delays a ect the ... is the activity (and, therefore, the **power**) to the ...
Cited by 19 - Web Search - BL Direct

Gate sizing for constrained delay/**power**/area optimization - group of 4 »

O Coudert - Very Large Scale Integration (VLSI) Systems, IEEE ..., 1997 - ieeexplore.ieee.org
... **power estimation**. The **toggle** rate depends on how the signals propagate in the **circuit**. In particular, resizing a **gate** affects the glitches, ie, the **transitions** ...
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Application of **Toggle**-Based **Power Estimation** to Module Characterization

G Jochens, L Kruse, W Nebel - Proceedings of PATMOS (Power and Timing Modeling of ..., 1997 - dice.ucl.ac.be
... switches is read from the **toggle** count output file, wire capacitances are ... cuits with a moderate **circuit**-depth an average error of about ... In this model the **power** ...
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Accurate logic-level **power estimation**

A Bogliolo, B Ricco, L Benini, G De Micheli - Low Power Electronics, 1995., IEEE Symposium on, 1995 - ieeexplore.ieee.org
... observing the switch- ing actstnty (**toggle** count) at the ... spurious tran- sitions (glitches), short **circuit** currents and ... impact on the to- tal **power** dissipation. ...
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Power estimation of cell-based CMOS circuits - group of 5 »

A Bogiolo, L Benini, B Ricco - Proceedings of the 33rd annual conference on Design ..., 1996 - portal.acm.org
... by looking at the switching activity (**toggle** count) and the ... parasitic phenomena (such as short-**circuit** currents, charging ... impact on the global **power**, cannot be ...
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Power compiler: a **gate**-level **power** optimization and synthesis system - group of 5 »

I Nedelchev - Proceedings of the 1997 International Conference on Computer ..., 1997 - doi.ieeecomputersociety.org
... The definition of internal and short-**circuit power** in section ... The internal **power** model is a nonlinear model based on ... is then multiplied by the **toggle** rate of ...
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Effects of delay models on peak **power estimation** of VLSI sequential circuits - group of 12 »

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